

#2
M.J.
2/19/02

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:) Group Art Unit: Unassigned
Wayne L. Cheung et al.) Examiner: Unassigned
Application No. Unassigned) Atty. Docket No. SJ09-2001-0140US1/
Filed: Herewith) IBM1P009
For: CMOS LOW-NOISE MR READ HEAD) Date: January 7, 2002
PRE-AMPLIFIER CIRCUIT)

)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on January 7, 2002.

Signed: 
Erica L. Mann

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.56 AND 1.97(b)

Commissioner for Patents
Washington, DC 20231

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1351 (Order No. SJ09-2001-0140US1/IBM1P009).

Respectfully submitted,
Silicon Valley IP Group

Kevin J. Zilka
Reg. No. 41,429

P.O. Box 721120
San Jose, CA 95172-1120
Telephone: (408) 971-2573